

Embedded Software | Sint-Katelijne-Waver

Voorbeeldlesrooster – fase 1

| | MAANDAG | DINSDAG | WOENSDAG | DONDERDAG | VRIJDAG |
|-----------|------------------------------|------------------------------------|---------------------------------|------------------------------------|-------------------------|
| 08.00 uur | | | | | |
| 09.00 uur | Computer sytems theorie | | OS fundamentals labo | Embedded fundamentals labo | |
| 10.00 uur | | | | | |
| 11.00 uur | | Electronics 1 theorie | | | Electronics 1 theorie |
| 12:00 uur | | | C programming fundamentals labo | | |
| 13.00 uur | Interface technology theorie | C Programming fundamentals theorie | | C Programming fundamentals theorie | Electronics 1 labo |
| 14.00 uur | | | | | |
| 15.00 uur | | | | Embedded fundamentals theorie | OS fundamentals theorie |
| 16.00 uur | | | | | |
| 17.00 uur | | | | | |